2025 International VLSI Symposium on Technology, Systems and Applications

TSA Call for Papers

April 21-24, 2025
Hsinchu, Taiwan

The 2025 International VLSI Symposium on Technology Systems and Applications (VLSI TSA) will be held on April 21-24, 2025 at the Ambassador Hotel Hsinchu, Taiwan. Original and unpublished papers on all aspects of advanced VLSI technology and applications are solicited.

SCOPE

■ **ADVANCED LOGIC TECHNOLOGY**
- Front-end Silicon CMOS and Foundry Platform Technology
- Advanced Process Modules and Nano-patterning, including EUV
- Advanced Packaging and 2.5D/3D Heterogeneous Integration
- Advanced Manufacturing Technology, Metrology and Yield

■ **NEUROMORPHIC and NOVEL COMPUTING**
- Neuromorphic Devices and Technologies for AI Hardware such as in-memory Computing

■ **POWER, MILLIMETER WAVE AND ANALOG TECHNOLOGY**
- Power and Analog/RF IC Device and Technology

■ **RELIABILITY OF SYSTEMS and DEVICES**
- Reliability Physics, Characterization and Test

■ **EMERGING DEVICE and COMPUTE TECHNOLOGY**
- Quantum Phenomena and Information Technologies
- Beyond Silicon CMOS Technologies such as Low Dimensional Material

■ **MODELING AND SIMULATION**
- Modeling and Simulation, including DTCO

■ **OPTOELECTRONICS, DISPLAYS, and IMAGING SYSTEMS**
- Silicon Photonics and Integrated Optoelectronic Devices

■ **MEMORY TECHNOLOGY**
- Advanced Memory: DRAM, FLASH, Emerging Memories such as Resistive, Spintronic and Ferroelectric Devices

■ **SENSORS, MEMS, and BIOELECTRONICS**
- MEMS, Imagers and Sensors
- Flexible and Organic Electronics

PAPER SUBMISSION GUIDELINES

1. Camera-ready manuscript (**2 pages** including figures and tables) should be submitted electronically in PDF format through the symposium website at https://expo.itri.org.tw/2025TSA/Submission

2. Accepted papers **MUST** be presented by one of the authors **in person** at the symposium to guarantee publication in the symposium proceedings. Presentations will be in English and will be limited to 15 minutes, with an additional 5 minutes for discussion.

3. Any changes on the title or author list or withdrawal after acceptance must be approved by Mentors or TSA Program Chairs.

4. All papers presented at the symposium will be published in the proceedings as submitted without revisions. Authors of accepted papers must transfer copyright to IEEE by utilizing the electronic IEEE Copyright Form (eCF) for inclusion in the IEEE Xplore database.

5. All paper presenters are required to register for the symposium

6. No-show papers will not be included in the symposium proceedings, nor will they be submitted to the IEEE Xplore database.

STUDENT SUBSIDY

- Student Travel Financial support for attending 2025 VLSI TSA is available for full-time student presenters living outside of Taiwan.

BEST STUDENT PAPER AWARD

The selection will be based on the paper quality evaluated by technical committee members, as well as the presentation of the paper at the symposium. The paper should be presented by the key author who is a full-time student at the time of paper presentation. The Best Student Paper Award will be presented to the winning student at the next year's symposium.

LATE NEWS PAPERS (Submission Start Date: December 23, 2024)

A very limited number of quality Late News Papers will be accepted. Note that Late News Papers are not eligible for the best student paper award.

IMPORTANT DATES (Note: All are based on Taiwan time, which is eight hours ahead of Greenwich Mean Time (GMT+8).)

Paper Submission Deadline	~~December 2, 2024~~ December 16, 2024
Late News Paper Submission Deadline	January 16, 2025
Notification of Acceptance	January 24, 2025
Notification of Acceptance (Late News Papers)	February 10, 2025
Final Paper Submission	February 28, 2025
Author Registration Deadline	February 28, 2025

TSA PROGRAM CHAIRS:

Shimeng Yu, Georgia Institute of Technology

Wei-Chung Lo, Industrial Technology Research Institute